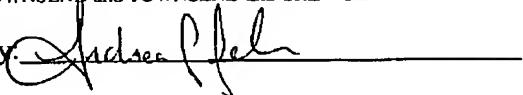


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TOWNSEND and TOWNSEND and CREW LLP

By: 

MAR 06 2006

PATENT

Attorney Docket No.: 015114-069200US

Client Ref. No.: A1256

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ketan Padalia et al.

Application No.: 10/716,309

Filed: November 17, 2003

For: TECHNIQUES FOR GROUPING
CIRCUIT ELEMENTS INTO LOGIC
BLOCKS

Customer No.: 26059

Confirmation No. 6902

Examiner: Nelson C. Lam

Technology Center/Art Unit: 2825

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed September 6, 2005, please enter the following amendments and remarks:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 9 of this paper.